SP-6183 US

Harsh D. Sharma PRIORITY DELAY INSERTION CIRCUIT

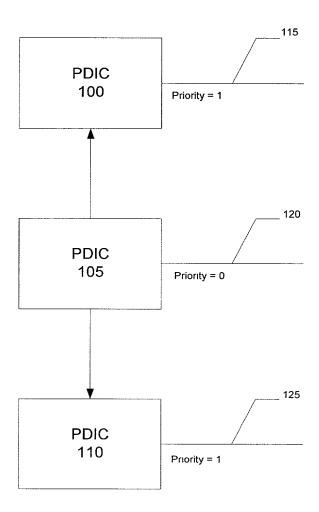


FIG. 1

SP-6183 US

Harsh D. Sharma PRIORITY DELAY INSERTION CIRCUIT

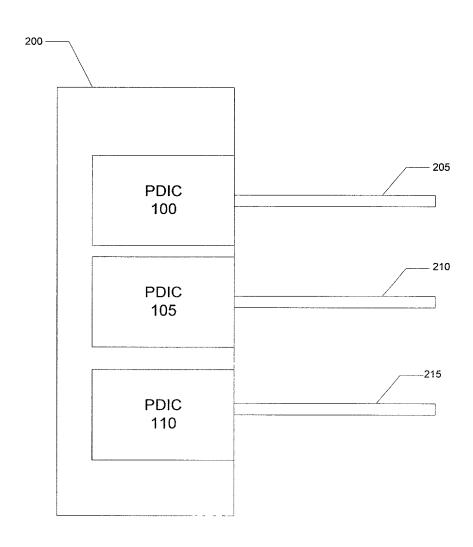


FIG. 2

SP-6183 US Harsh D. Sharma

PRIORITY DELAY INSERTION CIRCUIT

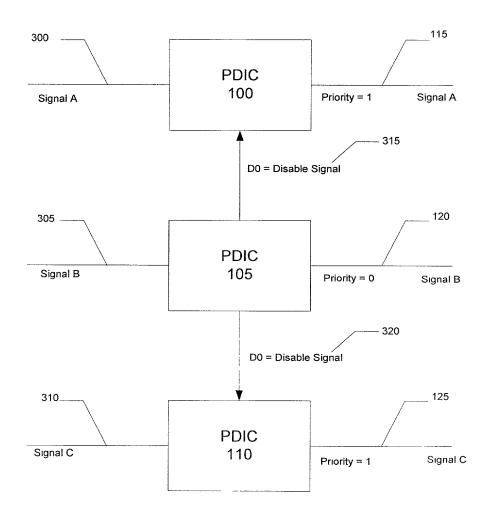
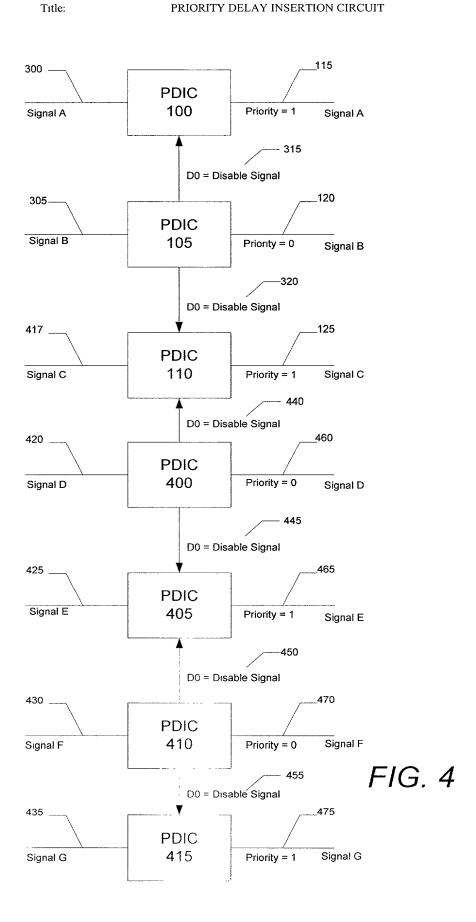


FIG. 3

SP-6183 US Harsh D. Sharma

PRIORITY DELAY INSERTION CIRCUIT



SP-6183 US Harsh D Sharma PRIORITY DELAY INSERTION CIRCUIT

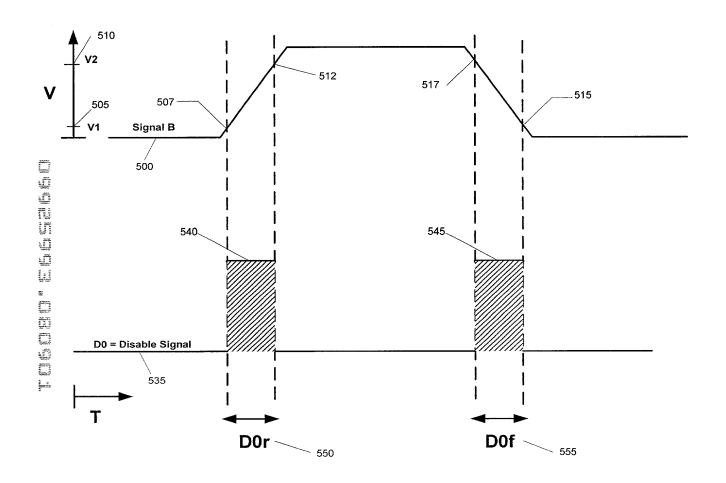


FIG. 5

SP-6183 US

Harsh D. Sharma
PRIORITY DELAY INSERTION CIRCUIT

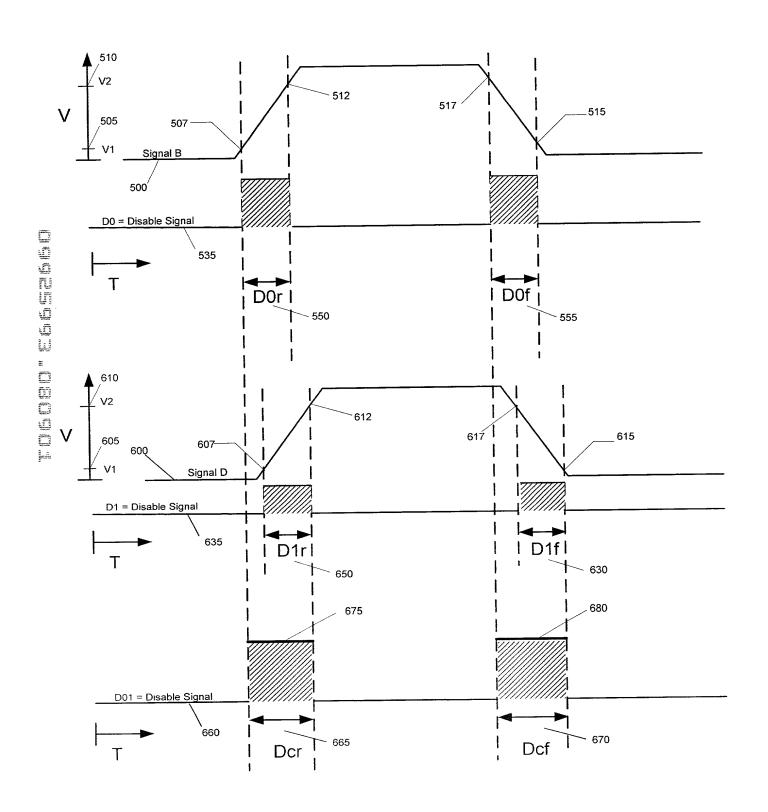


FIG. 6

SP-6183 US Harsh D. Sharma

PRIORITY DELAY INSERTION CIRCUIT

